

PRODUCT DATA SHEET

WF-7745

VOC-Free, No-Clean Wave Solder Flux

Introduction

WF-7745 is a water-based, VOC-free, halogen-free, no-clean wave solder flux designed for wave soldering through-hole, bottom-side surface mount and mixed-technology circuit boards. It performs well with both Pb-free and SnPb solders and processes. **WF-7745** is non-flammable, dramatically reducing volatile organic compound emissions and eliminating special flammable storage conditions. **WF-7745** easily passes the IPC TM-650 copper mirror test. It also passes the IPC TM-650 copper corrosion test, showing no evidence of green. This high degree of corrosion resistance is virtually unheard of with water-based no-clean fluxes. Even with this high degree of corrosion resistance, **WF-7745** solders as well as many halogen-containing, low solids, no-clean fluxes, yielding superior hole-fill and reduced solder balling.

Features

- ORLO per J-STD-004A
- Compatible with all common Pb-free and SnPb wave soldering alloys
- Superior hole fill
- No post-soldering green corrosion
- Low degree of solder balling
- Non-flammable and non-corrosive
- Can be shipped as non-hazardous

Physical Properties

WF-7745 is a colorless liquid, containing no rosin. The blend of water and VOC-free surfactants ensures even distribution of flux solids both during storage and during spray flux deposition. The specific gravity of **WF-7745**, 1.014 @ 25°C, is just slightly higher than that of pure water. However, in contrast to higher solids content fluxes, specific gravity is not the best method to assign quality standards for **WF-7745**. This is because the flux solids content is relatively low. While in-process quality control of **WF-7745** is not generally required, the best method is to ensure both solids content and activity level is by acid value titration.

Test	Result
Color	Clear
Specific Gravity: @25°C (77°F) @15°C (60°F)	1.014 1.014
Acid Value mgKOH/g flux mgKOH/g flux solids	39.3 916
Solids Content	4.29%
Flash Point (°F TCC)	None
J-STD-004A Flux Type	ORLO

Technical Support

Indium Corporation's internationally experienced engineers provide in-depth technical assistance to our customers. Thoroughly knowledgeable in all facets of Material Science as it applies to the electronics and semiconductor sectors, Technical Support Engineers provide expert advice in solder preforms, wire, ribbon, and paste. Indium Corporation's Technical Support Engineers provide rapid response to all technical inquiries.

Safety Data Sheets

The SDS for this product can be found online at <http://www.indium.com/sds>

From One Engineer To Another®

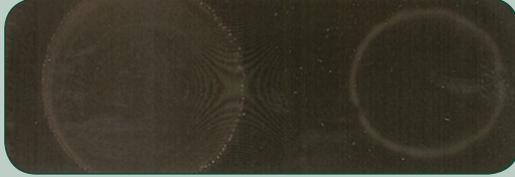


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TEST DATA

Copper Mirror



WF-7745

Standard Rosin

The J-STD-004A copper mirror test is performed per IPC-TM-650 method 2.3.32. To be classified as an "L" type flux, there should be no complete removal of the mirror surface. **WF-7745** shows only very minor removal of the copper mirror and, therefore, is classified as a ORLO.

Copper Corrosion

Copper corrosion is tested per IPC-TM-650 method 2.6.15. This test gives an indication of any visible reactions that take place between the flux residue after soldering and copper surface finishes. In particular, green copper corrosion should not be seen.



WF-7745 SnPb 0 Hours



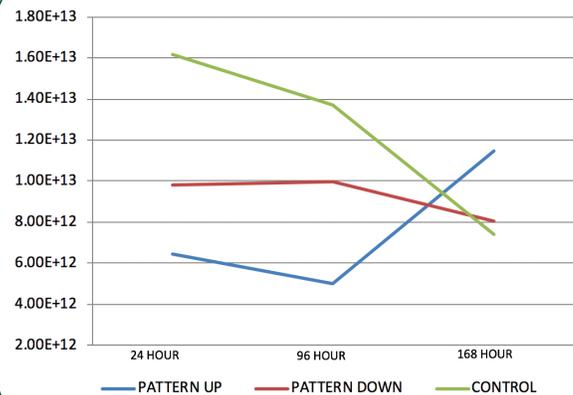
WF-7745 SnPb 240 Hours

Surface Insulation Resistance (SIR)

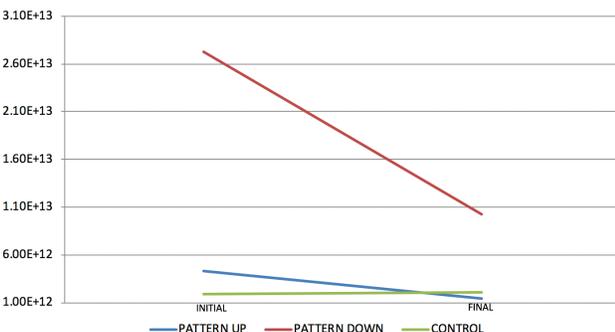
The Surface Insulation Resistance test is performed per IPC-TM-650 Method 2.6.3.3. All boards soldered with **WF-7745** pass the requirements of having exhibited no dendritic growth, no visible corrosion, and a minimum insulation resistance of 100 megaohms (1×10^8). The IPC-TM-650 SIR is a 7-day test and gives a general idea of the effect of the flux residue on the electrical properties of the surface of the circuit board.

IPC J-STD-004A SIR

	24 Hours	96 Hours	168 Hours
Pattern Up Mean	6.47E+12	5.00E+12	1.14E+13
Pattern Down Mean	9.81E+12	9.96E+12	8.04E+12
Control Mean	1.62E+13	1.37E+13	7.42E+12



Electrochemical Migration (ECM)



J-STD-004A Insulation Resistance Average Values

	Initial	Final
Pattern Up	4.34E+12	1.40E+12
Pattern Down	2.73E+13	1.02E+13
Control	1.93E+12	2.07E+12

The electromigration test is performed to IPC-TM-650 method 2.6.14.1 with boards prepared using IPC-TM-650 method 2.6.3.3. The test conditions for this test are 596 hours at $65^\circ\text{C} \pm 2^\circ\text{C}$ and $88.5\% \pm 3.5\%$ RH. To pass this test, there should be no visible corrosion and no dendritic growth that decreases line spacing by more than 20%. In addition, the insulation resistance should not drop more than one order of magnitude after the first 96-hour stabilization period when a bias voltage is applied.



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PERFORMANCE AND PROCESS DATA

Hole Fill



Indium Corporation uses several of its own tests, based on IPC workmanship standards, for determining hole fill. Depending on the design criteria for the flux, Indium Corporation uses holes of varying sizes and circuit board finishes. However, we always look for 100% hole fill, even though the IPC recognizes that a smaller degree of hole fill is acceptable for its workmanship standards. Testing is typically performed with both lead-free (Indium Corporation's Sn995 alloy) and tin-lead (63Sn/37Pb) solders.

Soldering Performance*

	Pb-Free	SnPb
100% PTH Fill Yield	96%+	99%+
* 0.062-inch Indium test board 7–20mil diameter PTH		

Shelf Life

The shelf life for this product is **3 years** in an unopened container stored at less than 90°F/32°C. Shelf life for an opened container will vary depending on storage conditions, including open time, temperature, and humidity.

Process Recommendations

Indium Corporation profiles all of its wave soldering fluxes on its own lead-free and tin-lead wave soldering machines prior to making them available to the market.

62 mil-thick Circuit Board Process Recommendations

Flux Deposition Rate µg/in ² solids	Preheat Temp		Preheat Time (sec)	Alloy	Contact Time (sec)	Pot Temp (°C)
	Top (°C)	Bottom (°C)				
1,000–1,700	110–135	135–150	75–150	SnCu	4–5	265–270
500–1,700	100–125	125–135	75–150	SnPb	2–3	250–260

Residue Removal Recommendations

All of Indium Corporation's no-clean fluxes, including this formula, are designed to be electrically safe under normal consumer electronic and telecommunication operating conditions. Unless otherwise specified, electrically safe means that the post soldering residues pass J-STD-004A SIR and ECM testing. However, it is understood that some customers desire to remove residues for cosmetic reasons, improved in-circuit testing, improved compatibility with specific conformal coatings, or where the operating parameters of the circuit board may be in extreme conditions for a prolonged period.

If the removal of no-clean flux residues is desired, most commercially available cleaning agents will be effective. Indium Corporation's Technical Support Engineers work closely with cleaning agent vendors and have confirmed flux residue removal capabilities from several vendors using their recommended products and parameters. It is unlikely that users of Indium Corporation's no-clean products will need to change their current residue removal materials and parameters from those currently used. However, when establishing a new process or desiring confirmation of process recommendations, please contact Indium Corporation's Technical Support Engineers for assistance.



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Indium Corporation Compatible Products

- **Solder Paste:** Indium8.9HF
- **Cored Wire:** CW-807
- **Flux Pen:** NC-771 (or FP-500)

Indium Corporation has a long history of testing soldering product compatibility and has designed its solder pastes, wave fluxes, cored wires, and rework fluxes so that they are compatible with each other. Based on our experience, we have learned that Indium Corporation's products, which have been individually designed to meet the requirements of a certain specification such as IPC J-STD-004B, when combined, will yield test results meeting the same requirements, as typically determined by Surface Insulation Resistance (SIR) and Electromigration (ECM) testing. It is also possible that competitors' products and those that cross different specification revisions; such as an Indium Corporation solder paste tested to J-STD-004, a competitor Wave flux tested to J-STD-004A and an Indium Corporation Cored Wire tested to the requirements of J-STD-004B, will also be compatible when tested under one of the above versions, but it is not as certain. In these cases, where there is doubt, we prefer to run actual testing to confirm compatibility. Indium Corporation maintains a small library of these test results, which are made available to its customers. While the safest way to ensure product compatibility is by using a complete line of Indium Corporation compatible products, if you have questions, regarding the compatibility of a specific set of products not listed below, please contact Indium Corporation's Technical Support Engineers.

Health, Safety, Environmental, and Shipping

RoHS

This product complies with the requirements set under the EU Directive 2002/95/EC. This product does not contain any metals or flame retardants.

Hazard Label



WHMIS Label

WHMIS:
Not classified

DOT Classification

- Transport in accordance with applicable regulations and requirements
- Non-hazardous for shipping
- North America Emergency Guide Book: Not applicable
- UN: None

Additional Information

* J-STD-004B is the IPC Joint Industry Standard for classifying and testing soldering fluxes. It varies from the prior versions, J-STD-004 and J-STD-004A, in two very important ways. J-STD-004B uses a modified electromigration (ECM) test battery which is designed to better test the effects of the flux in high humidity conditions at normal operating temperatures and voltages. The environmental test is specifically designed to try to create dendritic growth and create failure in marginal flux formulas, unlike the prior version of J-STD-004 which used higher temperatures and voltages that did not grow dendrites as easily. Also, J-STD-004B halogen testing now reveals the total amount of halogen in a flux by first using an oxygen bomb to disassociate any halogen from the chemical compounds that they are bound to, and then collecting and quantifying them. Prior versions of J-STD-004 were unable to detect halogens that were present, but only disassociated at high temperatures (such as soldering temperature). As such, prior testing methods might give the user a false sense that no halogens are present in the flux, when in fact they are. Indium Corporation strongly supports the enhanced features of J-STD-004B because it better serves the users need for information.

This product data sheet is provided for general information only. It is not intended, and shall not be construed, to warrant or guarantee the performance of the products described which are sold subject exclusively to written warranties and limitations thereon included in product packaging and invoices. All Indium Corporation's products and solutions are designed to be commercially available unless specifically stated otherwise.

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